

I claim:

1. A method for transmitting data in a motor vehicle having a transmitter and a receiver, which comprises the steps of:

splitting a signal which is to be transmitted, into a constant part and a variable part; and

combining a partial value of the constant part with the variable part and transmitting a result signal to the receiver.

2. The method according to claim 1, which comprises:

dividing the constant part by a predefinable value N resulting in N divided values; and

summing each of the divided values with the variable part forming the result signal to be transmitted.

3. The method according to claim 1, which comprises determining the constant part by a low-pass filtering of the signal which is to be transmitted.

4. The method according to claim 1, which comprises applying a correction factor to the constant part.

5. The method according to claim 4, which comprises forming the correction factor by summing N values to be transmitted, minus the constant part.

6. The method according to claim 1, which comprises transmitting the constant part as a digital word, the constant part being divided into M identical word parts where  $M \geq 2$ , and in each case a word part of the constant part is transmitted in combination with the variable part so that after in each case M transmissions a value of the constant part is transmitted.

7. The method according to claim 2, which comprises adding the N divided values transmitted to the receiver in the receiver to determine the constant part, the constant part is divided by N in the receiver resulting in a further divided value and the further divided value is subtracted from the result signal received in order to obtain the variable value.

8. The method according to claim 1, which comprises forming the variable part by subtracting the constant part from the result signal transmitted.

9. A configuration for transmitting data in a motor vehicle, comprising:



a subtracting element receiving the transmitted data-reduced signal and an output signal of said divider and having an output where the variable signal can be tapped, said subtracting element connected to said divider.

11. The configuration according to claim 9, wherein said transmitter has means for correcting the constant part.

12. The configuration according to claim 11, wherein said means for correcting the constant part includes:

a summing element for summing N data-reduced signal values and outputting an output signal;

a further subtracting element connected to said summing element and to which the output signal of said summing element and the constant part are fed, said further subtracting element outputting an output signal; and

a further adding element connected to said further subtracting element and to said extraction means, the constant part and the output signal of said subtracting element are fed to said further adding element and said further adding element having an output connected to said divider.

13. The configuration according to claim 9, wherein said extraction means includes a low-pass filter.

14. The configuration according to claim 9, wherein said transmitter and said receiver form part of a microprocessor.

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